

## **REMARKS**

In response to the above identified Office Action, Applicants respectfully request reconsideration in view of the following remarks. Applicants do not add, cancel, or amend any claims. Accordingly, claims 1-23 are pending.

### **I. Claims Rejected Under 35 U.S.C. § 102**

Claims 1, 7 and 13 stand rejected under 35 U.S.C. § 102 as being anticipated by U.S. Patent No. 6,647,464 issued to Reidlinger, et al (hereinafter "Reidlinger"). Applicants respectfully disagree for the following reasons.

In regard to claims 1, 7, and 13, these claims include the elements of "splitting a cache operation into two or more phases and two or more clock domains." The Examiner cites Fig. 2 of Reidlinger as teaching the elements of this claim. However, the Applicants have reviewed Fig. 2 and Reidlinger in general and have been unable to discern any part therein that teaches these elements of claims 1, 7 and 13. Specifically, the Applicants have been unable to discern any part therein that teaches splitting a cash operation into two or more clock domains. Rather, Reidlinger teaches a system of cache operation that utilizes both the low and high phases of a single clock. See Reidlinger, col. 6, line 3-55. Thus, Reidlinger discloses use of a single clock domain and not multiple clock domains as recited in claims 1, 7 and 13. Thus, Reidlinger does not teach each of the elements of these claims. Accordingly, reconsideration and withdrawal of the anticipation rejection of these claims are requested.

## II. Claims Rejected Under 35 U.S.C. § 103

Claims 2-6, 8-10 and 14-18 stand rejected under 35 U.S.C. § 103 as being unpatentable over Reidlinger in view of U.S. Patent No. 6,732,236 issued to Favor, et al. (hereinafter “Favor”). These claims depend from independent claims 1, 7 and 13 and incorporate the limitations thereof. Thus, at least for the reasons mentioned above in regard to independent claims 1, 7 and 13 Reidlinger does not teach or suggest each of the elements of these claims. Favor does not cure the defects of Reidlinger. Specifically, Favor does not teach or suggest the use of multiple clock domains for a cache operation. Thus, Reidlinger in view of Favor does not teach or suggest each of the elements of these claims. Accordingly, reconsideration and withdrawal of the obviousness rejection of these claims are requested.

Claims 11, 12 and 19-23 stand rejected under 35 U.S.C. § 103 as being unpatentable over Reidlinger in view of Favor and in further view of “Intel 865PE/865P Chipset Datasheet,” hereinafter “Datasheet.”

Claims 11 and 12 depend from independent claim 7 and incorporate the limitations thereof. Thus, at least for the reasons mentioned above in regard to independent claim 7 Reidlinger and Favor do not teach or suggest each of the elements of this claim. Further, the Examiner has not relied upon Datasheet to cure these defects of Reidlinger and Favor. Specifically, the Examiner has not relied upon Datasheet to disclose the use of multiple clock domains. Thus, the Examiner has not established a *prima facie* case of obviousness for claims 11 and 12. Accordingly, reconsideration and withdrawal of the obviousness rejection of these claims are requested.

In regard to claim 19-23 these claims include elements similar to those mentioned above in regard to independent claims 1, 7 and 13. Specifically, the use of multiple clock domains for a

single cache operation. Thus, for the reason mentioned above in regard to independent claim 1, 7 and 13, Reidliger, Favor and Datasheet do not teach or suggest each of the elements of these claims. Accordingly, reconsideration and withdrawal of the obviousness rejection of claims 19-23 are requested.


### CONCLUSION

In view of the foregoing, it is believed that all claims now pending, namely claims 1-23, patentably define the subject invention over the prior art of record, and are in condition for allowance and such action is earnestly solicited at the earliest possible date. If the Examiner believes that a telephone conference would be useful in moving the application forward to allowance, the Examiner is encouraged to contact the undersigned at (310) 207 3800.

Respectfully submitted,

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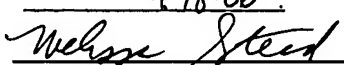
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